

Document Revision History

Date	Doc. Rev.	Notes
2018-05-07	Rev. 1.0	Errata #1: Added
2018-05-29	Rev. 1.1	Errata #1: Added link to workaround when using CSI Camera Module 5MP OV5640

Overview

Errata #1: I2C signals (I2C3) shared between CSI connectors.....4

Errata #1: I2C signals (I2C3) shared between CSI connectors

Affected Version: **Apalis TK1 Mezzanine V2.0**Fixed in: **No redesign scheduled****1.1 Description**

The Apalis TK1 Mezzanine CSI connectors X5, X6, X7 share same I2C signals (I2C3). It is therefore not straight forward to address multiple CSI cameras with the **same I2C slave address**. For some CSI cameras it might not even be possible.

It is possible to connect multiple Toradex CSI Camera Module 5MP OV5640 with Apalis TK1 Mezzanine V2.0 at the same time as this device supports changing the I2C slave address. More details can be found on our developer website: https://developer.toradex.com/csi-camera-module-5mp-ov5640-linux#Dual_Camera_Use_Case

1.2 Workaround

Cameras which aren't supporting I2C slave address changes can be used by making a few hardware assembly changes on the Apalis TK1 Mezzanine board. For development and evaluation purposes, customers can de-solder the zero ohm resistors connected in series with I2C signals of the CSI connectors and other I2C signals could be patch wired. I2C1 and I2C3 signals are available on the mezzanine board.

Please refer to Apalis TK1 Mezzanine V2.0 schematics and assembly drawing for more details: <https://developer.toradex.com/products/apalis-tk1-mezzanine#design-resources>

DISCLAIMER:

Copyright© Toradex AG. All rights reserved. All data is for information purposes only and not guaranteed for legal purposes. Information has been carefully checked and is believed to be accurate; however, no responsibility is assumed for inaccuracies.

Brand and product names are trademarks or registered trademarks of their respective owners. Specifications are subject to change without notice.