


	1	2	3	4	5	6	7	8	
A	<div>REVISION HISTORY</div> <div>IF IN DOUBT ASK</div> <div>1. Design Revision V1.0A: Preliminary design. Date: 28th Feb 2017</div> <div>2. Design Revision V1.1A: - PowerSupply.SchDoc: Ferrite Beads L1, L2, L3 and L4 have been replaced with 0 ohm resistors R18, R19, R20, R21 - CameraSensor.SchDoc: CAM_STROBE signal has been pulled down using 10 Kohm resistor R6 Date: 27nd June 2017</div> <div>3. Design Revision V1.1A : - PowerSupply.SchDoc: Resistors R2 and R3 positions have been swapped in the schematic due to a mistake in the silkscreen designator placement on the PCB. Date: 22nd August 2017</div> <div>4. Design Revision V1.1B : - CameraSensor.SchDoc: Updated assembly variant; Oscillator OCS1, Resistor R15 and Capacitor C21 are assembled by default. Schmitt-Trigger Buffer IC4 and Capacitor C14 are not assembled. Date: 25th September 2018</div> <div>5. Design Revision V1.1B : - CameraSensor.SchDoc: Capacitors C17 and C19 positions have been swapped in the schematic due to a mistake in the silkscreen designator placement on the PCB. Date: 12th December 2018</div>							<div>Hardware Architecture Schematic</div> <div>HardwareArchitecture.SchDoc</div> <div></div>	A
B								B	
C								C	
D								D	
	1	2	3	4	5	6	7	8	



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Title: <b>CSI Camera Module SMP OV5640</b>			Toradex AG Altsagenstrasse 5 Horw 6048 Switzerland	
Size: A3	Number: 1.	Revision: V1.1		
Date: 12.12.2018	Time: 11:30:41	Sheet 1 of 4		
File: RevisionHistory.SchDoc				


1	2	3	4	5	6	7	8
A							A
B							B
C							C
D							D
1	2	3	4	5	6	7	8

Camera Sensor (OV5640) Schematic  
CameraSensor.SchDoc



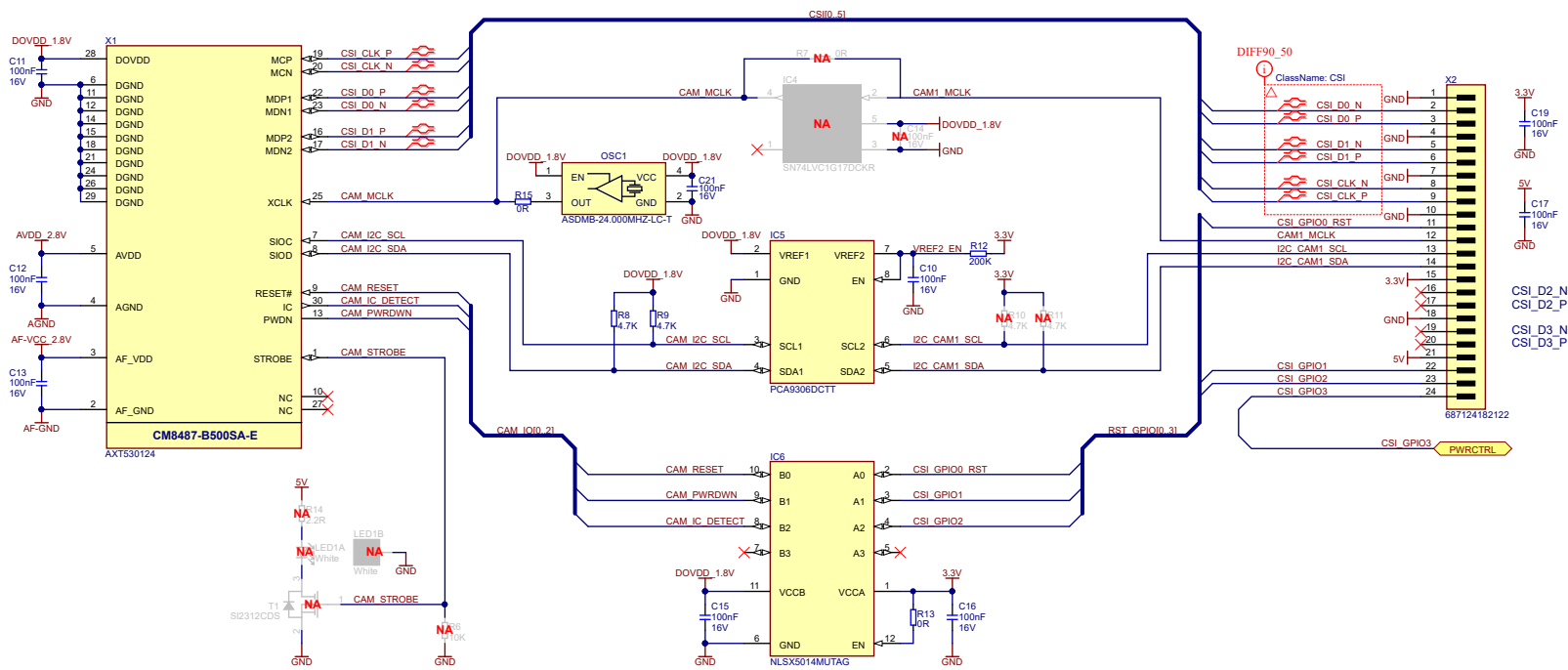
Camera Sensor (OV5640) Schematic  
PowerSupply.SchDoc



  
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Title: <b>CSI Camera Module SMP OV5640</b>			Toradex AG Altsagenstrasse 5 Horw 6048 Switzerland
Size: A3	Number: 2.	Revision: V1.1	
Date: 12.12.2018	Time: 11:30:41	Sheet 2 of 4	
File: HardwareArchitecture.SchDoc			





Alternative Components :  
Texas Instruments : TXB0304RUTR

Assembly options for CAM\_MCLK:  
Option 1: Using external oscillator OSC1: assemble OSC1, C21 and R15; unassemble IC4, C14 and R7  
Option 2: Using clock source from Colibri/Apalis module: assemble IC4, C14; unassemble OSC1, C21, R15, R7

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Title: <b>CSI Camera Module SMP OV5640</b>			Toradex AG Altsägenstrasse 5 Horw 6048 Switzerland	
Size: A3	Number: 4	Revision: V1.1		
Date: 12-12-2018	Time: 11:30:41	Sheet 4 of 4		
File: CameraSensor.SchDoc				